

EAST - [default.wsp:1]

File
View
Edit
Tools
Window
Help

Drafts

Pending

Active

Failed

Saved

Favorites

Imaged (0)

L1: (36) multilevel adj non adj volatile

L2: (7749) multilevel and reference

L3: (845) 2 and (non-adj volatile)

L4: (205) 3 and decoder

L5: (40) 4 and (multi adj bit)

L6: (5) 4 and (365/210.ccls.)

Search

Low

Browse

Queue

Clear

DBs

USPAT, EPO, JPD, DERWENT, USOCR

Plurals

Default operator:

OR

Highlight all hit terms initially

4 and (365/210.ccls.)

BRS f...

IS&R...

Image

Text

HTML

| | U | I | Document ID | Issue Date | Pages | Title | Current OR | Current XRef | R |
|---|--------------------------|--------------------------|--------------|------------|-------|--|------------|----------------------------|---|
| 1 | <input type="checkbox"/> | <input type="checkbox"/> | US 5936906 A | 19990810 | 13 | Multilevel sense device for a flash memory | 365/210 | 365/185.03; 365/185.21 | 3 |
| 2 | <input type="checkbox"/> | <input type="checkbox"/> | US 5828616 A | 19981027 | 16 | Sensing scheme for flash memory with multilevel cells | 365/210 | 365/184; 365/185.03; | 3 |
| 3 | <input type="checkbox"/> | <input type="checkbox"/> | US 5774395 A | 19980630 | 23 | Electrically erasable reference cell for | 365/185.3 | 365/185.03; 365/185.24; | 3 |
| 4 | <input type="checkbox"/> | <input type="checkbox"/> | US 5748546 A | 19980505 | 17 | Sensing scheme for flash memory with multilevel cells | 365/210 | 365/184; 365/185.03; | 3 |
| 5 | <input type="checkbox"/> | <input type="checkbox"/> | US 5594691 A | 19970114 | 19 | Address transition detection sensing interface for flash | 365/185.03 | 365/185.21; 365/189.05; | 3 |

Start

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2:20 PM